

Figure 1

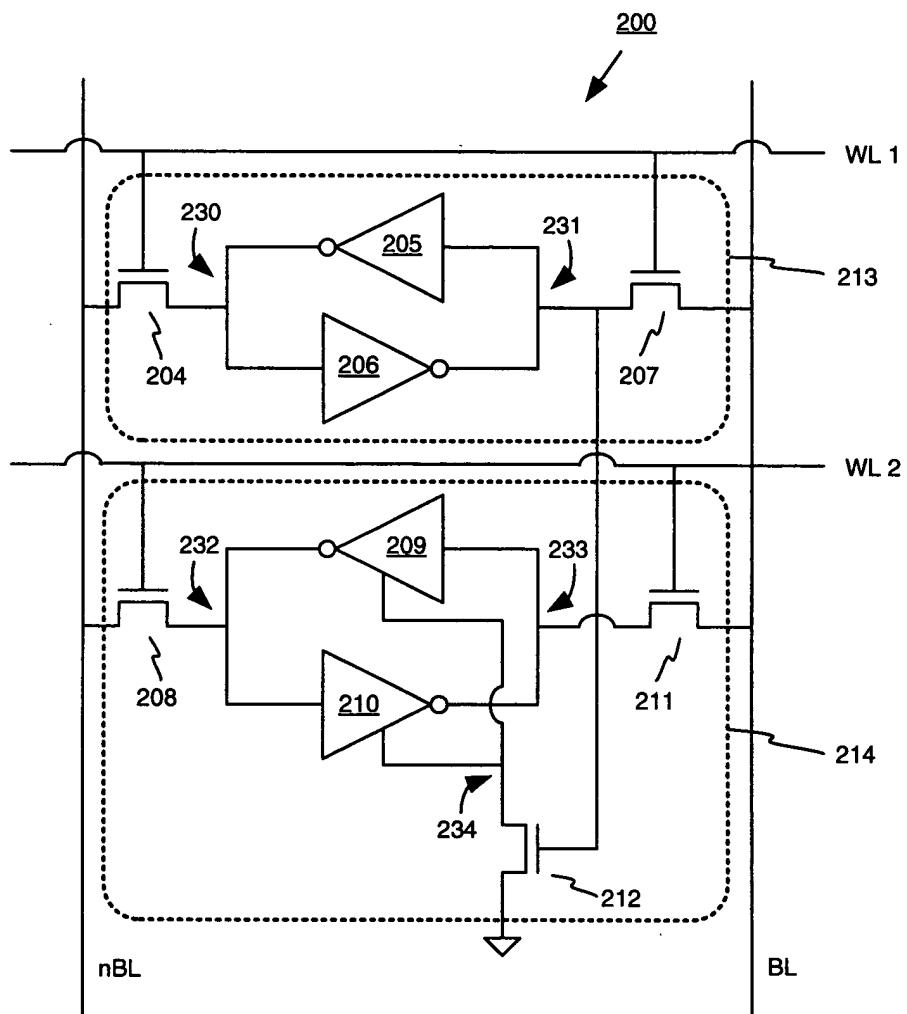


Figure 2

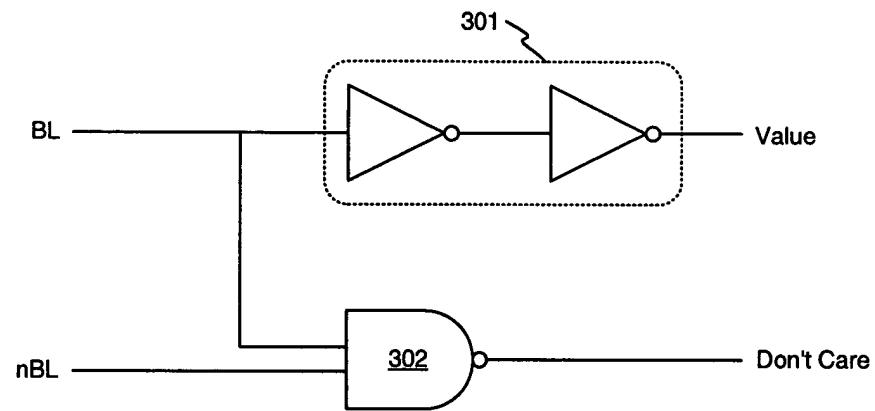


Figure 3

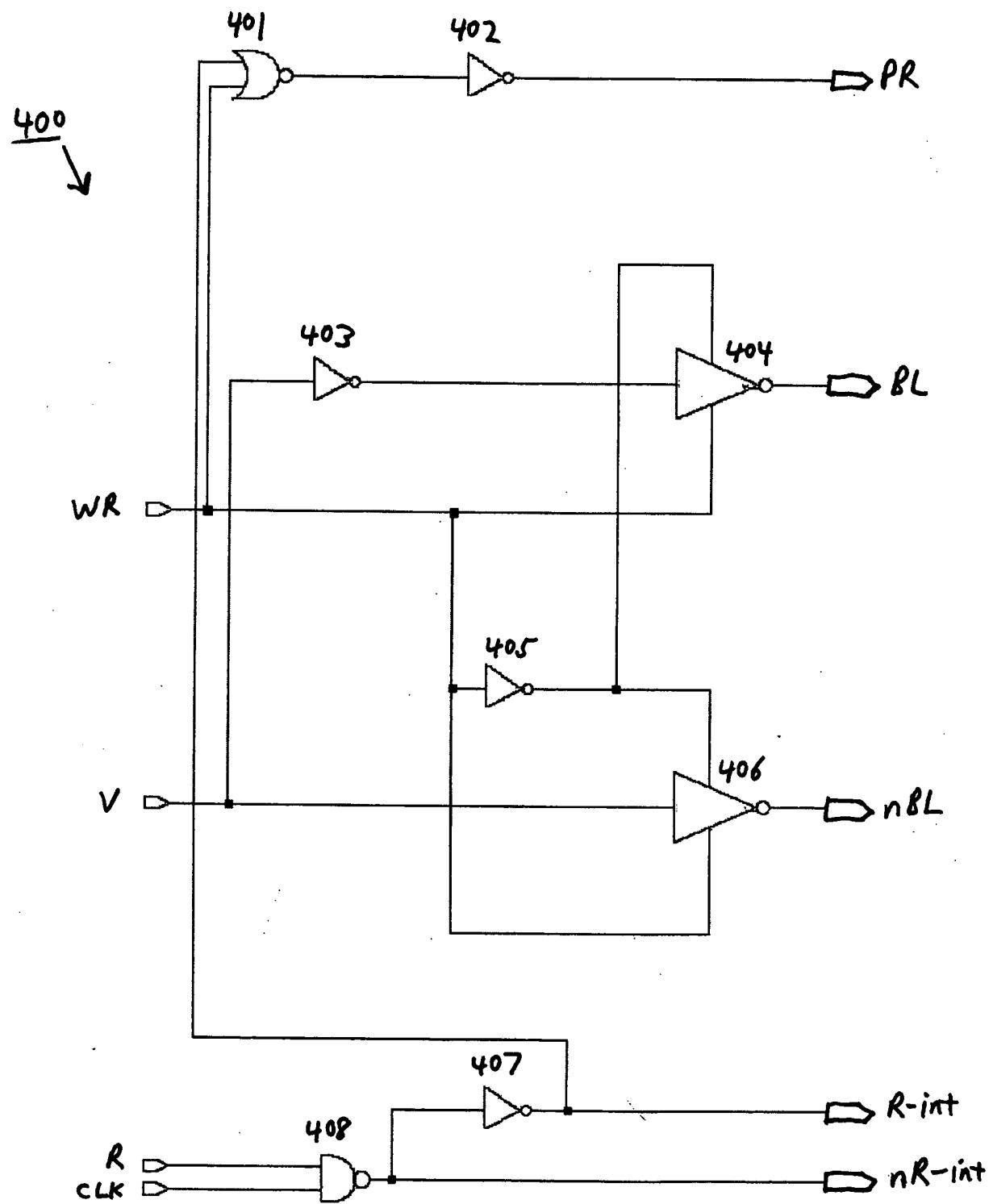


Figure 4

(to/from dual-bit memory cells 200)

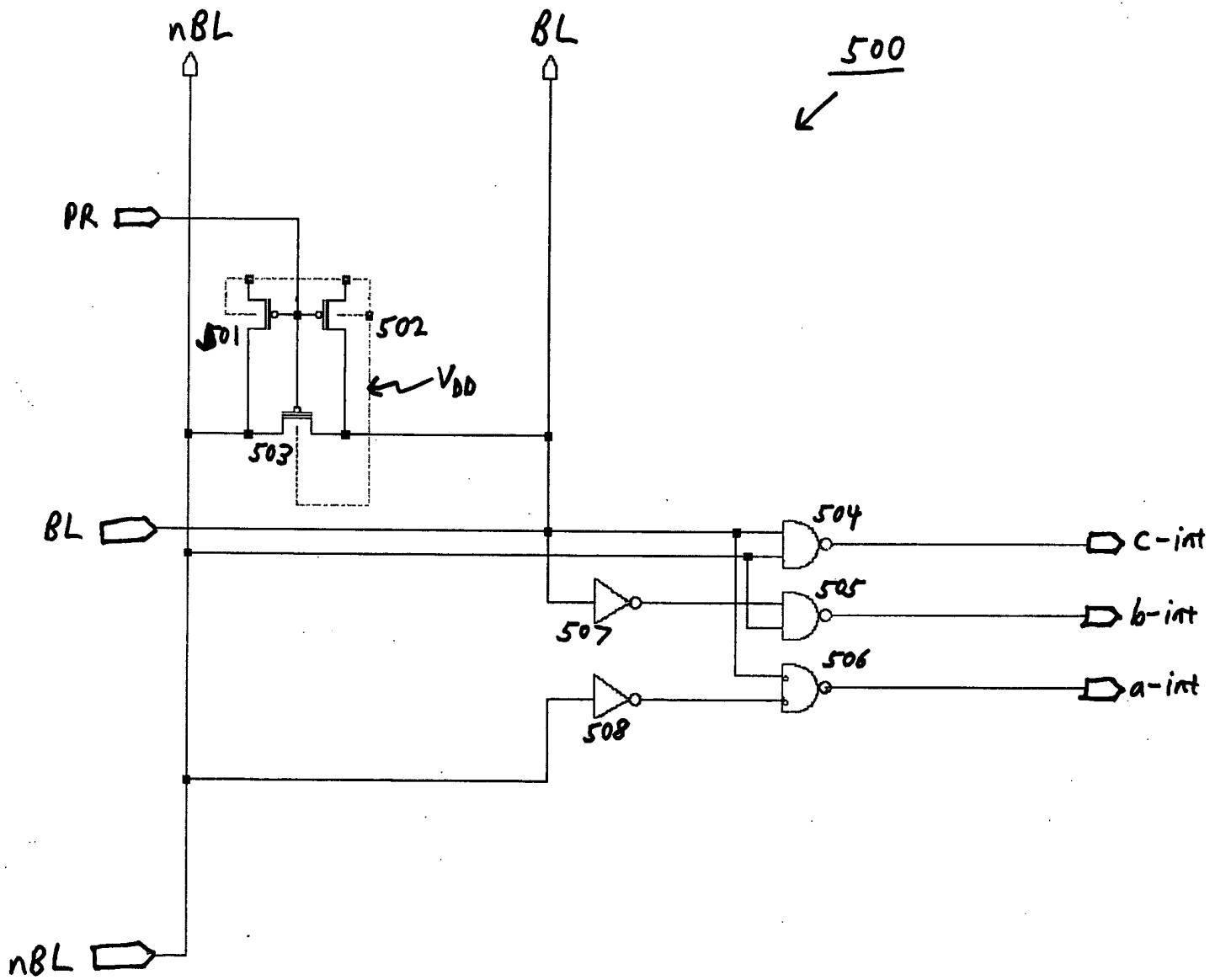


Figure 5

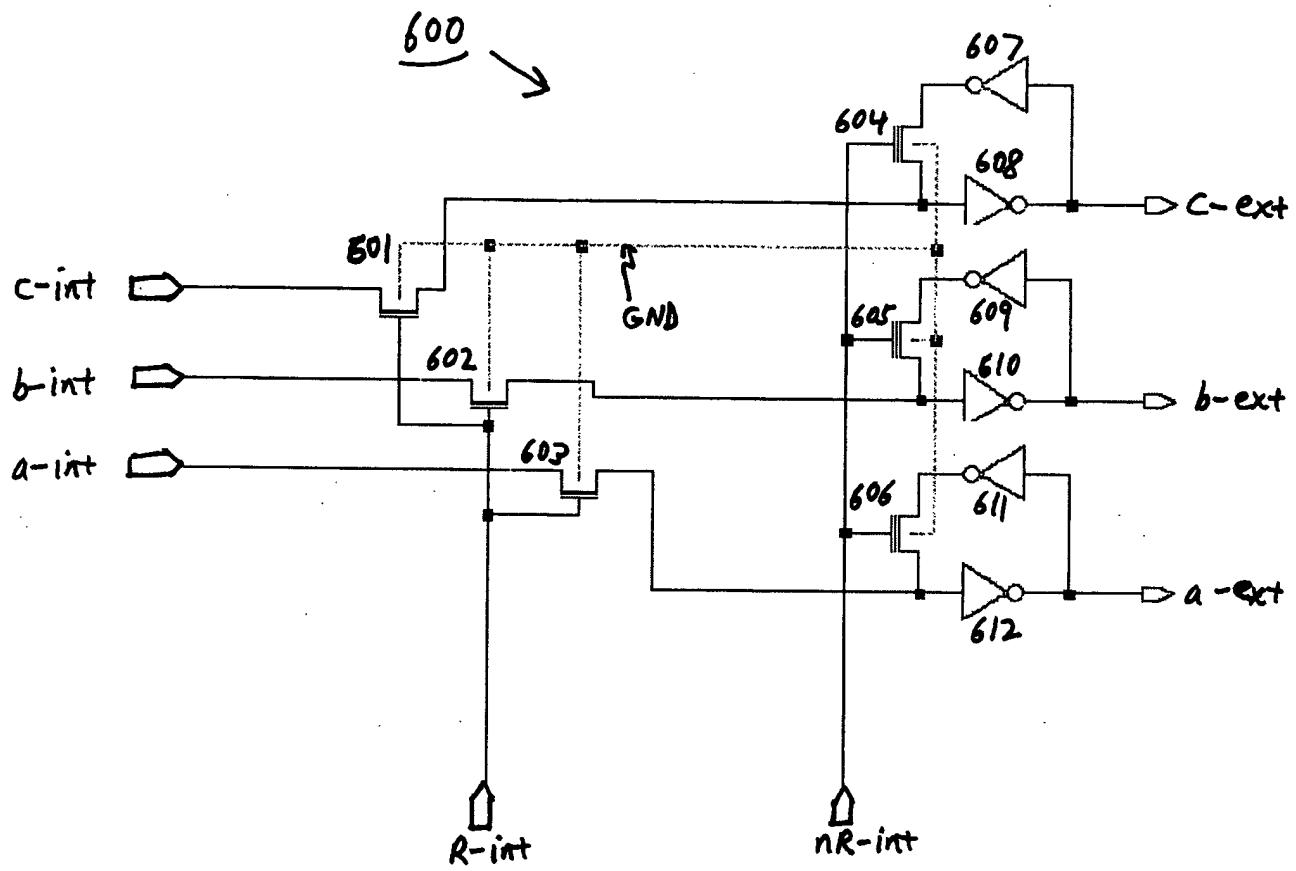


Figure 6